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**APPLICANT(S):** W. Rhee et al.

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**FOR:** VOLTAGE-CONTROLLED DELAY CIRCUIT USING  
SECOND-ORDER PHASE INTERPOLATION

**ASSOCIATE POWER OF ATTORNEY**

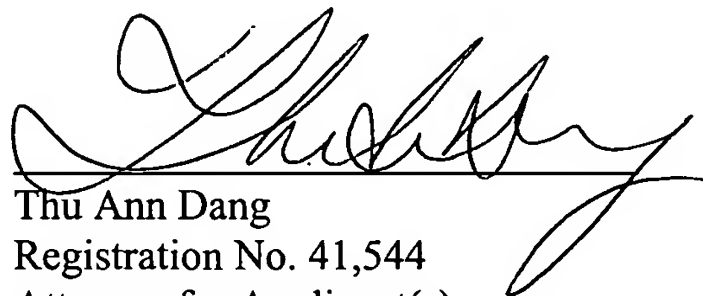
Please recognize **JOSEPH B. RYAN**, Reg. No. 37,922; **KEVIN M. MASON**, Reg. No. 36,597; **WILLIAM E. LEWIS**, Reg. No. 39,274; **ROBERT J. MAURI**, Reg. No. 41,180; **WAYNE L. ELLENBOGEN**, Reg. No. 43,602; **MICHAEL J. CHANG**, Reg. No. 46,611; and **ROBERT W. GRIFFITH**, Reg. No. 48,956; each of them of **RYAN, MASON & LEWIS, LLP**, 90 Forest Avenue, Locust Valley, New York 11560 as associate attorneys in the above-mentioned application, with full power to prosecute said application, to make alterations and amendments therein, and to transact all business in the Patent and Trademark Office connected therewith.

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